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FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	1	of		1

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Com	place if Knawo 1547402
Application Number	Unassigned 71/497
Filing Date	December 10, 2004
First Named Inventor	Hirofumi Totsuka et al.
Examiner Name	Unassigned
Attorney Docket Number	032404-082

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STATUS											
Examiner Initials	Document Number	Kind Code (if known)	Country	Date of Publication (MM-DD-YYYY)	Translation	Partial Translation	Eng. Lang. Summary	Search Report	IPER	Abstract	Cited in Spec
/I.P./	*JP 10-145348	Α	Japan	05-29-1998			-	•			\Box
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NON-PATENT LITERATURE DOCUMENTS						
Examiner Initials	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
/J:P./	*ANSGAR POTTBACKER ET AL., "A Si Bipolar Phase and Frequency Detector IC for Clock Extraction up to 8GB/s", IEEE Journal of Solid State Circuits", December 1992, Vol. SC-27, pp. 1747-1751.					

*Copy Attached

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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SECOND

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

	Complete if Known	/0
Application Number	10/517,493	7
Filing Date	December 10, 2004	4
First Named Inventor	Hirofumi Totsuka et a	12
Examiner Name	JAYANTI K PATEL	TO
Attorney Docket No.	1032404-000082	~

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Examiner Signature /Jason Perilla/ Date Considered 10/30/2007	
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